

## **TENDER FORM**

1. I/we hereby tender to construct, completed and maintain the work specified in the under written memorandum at the tendered rates quoted in SCHEDULE 'A'

The total amount of which comes to Rs. ----- or at ----- percentage Below / Above in the rates specified in SCHEDULE 'B' . The total amount of which comes to Rs. ----- ( INCLUDING GST) in accordance with the said conditions of contract specification, bills of Quantities,

## General Description of work : Electronics Kit

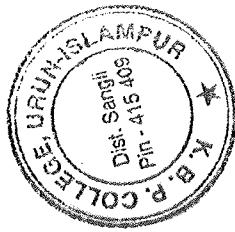
- A. Estimated Cost. ( Included all taxes) : Rs.
- B. EMD. 1% Total Cost :Rs.
- C. Security Deposit  
5% of Estimated amount: : Rs.  
At time of agreement
- D. Period for the completion of work : **1 months ( Including rainy season)**
- 2. I/we undertake, if my /our Tender is accepted to commence the work and complete and handover the same within the time limit stipulated in the appendix and the memorandum here above.
- 3. I/we agree to abide by this Tender for period of 90 Days from the date of submission of the same audit remain binding on us and may be accepted at any time before the expiry of any further period extended by mutual consonant.
- 4. Until and unless a formal agreement is prepared and executed this Tender, Together with you written acceptance thereof shall constitute a binding contract between us together with all the conditions of contract, specifications, schedule of quantities, appendix drawings, specification General rules and instructions to Tenders,
- 5. In case of default by me/us in our obligations in paragraphs(2),(3) and (4) here above. I/we acknowledge, you are at liberty to fore it the Earnest money deposited herewith.

**Seal and signature of the Contractor.**

**Note : Please attach self attested copy of your business licences, as applicable.**

**Mahatma Phule Shikshan Sanstha's  
Karmaveer Bhaurao Patil College, Urun Islampur  
BCA Department**  
**LABORATORY EQUIPMENTS REQUIREMENT**  
**Year 2025-2026**

Sr. No.	Equipment Name	QTY	Rate	Remark
1	Circuit of AND, OR, NAND, NOR, EX-OR, EX-NOR, Invert and Buffer gates Kit	2		
2	3 Bit Binary to Grey Code	2		
3	4 Bit Binary to Grey Code	2		
4	Half Adder	2		
5	Full Adder	2		
6	Half Subtractor	2		
7	Full Subtractor	2		
8	Multiplexer	2		
9	Demultiplexer	2		
10	Flip-Flops- SR Flip- Flop, D Flip-Flop, J-K Flip-Flop, T Flip,Flop.	1		
11	Single Bit magnitude Comparator	2		
12	Inegrator and differentiator	2		
13	FM Modulation & Demodulation	2		
14	Function Generator (Aplab)	1		
15	DSO Dual Slope	2		



  
**PRINCIPAL**  
 Karmaveer Bhaurao Patil College  
 Urun-Islampur (Sangli)